

MAHARASHTRA STATE BOARD OF TECHNICAL EDUCATION, MUMBAI TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES																
COURSE NAME : ELECTRICAL ENGINEERING GROUP																
COURSE CODE : EE																
DURATION OF COURSE : SIX SEMESTERS										WITH EFFECT FROM 2012 - 13						
SEMESTER : FIFTH										DURATION : 16 WEEKS						
PATTERN : FULL TIME										SCHEME :						
SR. NO.	SUBJECT TITLE	Sub. Title Abbr	SUBJECT CODE	TEACHING SCHEME			PAPER HRS	EXAMINATION SCHEME								SW
				TH	TU	PR		TH(1) MAX	MIN	PR(4) MAX	MIN	OR(8) MAX	MIN	TW(9) MAX	MIN	
1	Behavioral Science			01	--	02	--	--	--	--	25#	10	25@	10	50	
2	Switchgear & Protection			03	--	02	03	100	40	--	--	--	--	25@	10	
3	A.C. Machines			03	--	02	03	100	40	50#	10	--	--	25@	10	
4	Industry Electrical Systems II			04	--	--	03	100	40	--	--	--	--	--	--	
5	Microcontroller & Applications			03	--	02	03	100	40	--	--	25#	25	25@	10	
6	Energy Conservation & Audit			03	--	02	03	100	40	--	--	--	--	25@	10	
7	Entrepreneurship Development & Project			01	01	02	--	--	--	--	--	--	--	25@	10	
8	Professional Practices III			--	--	03	--	--	--	--	--	--	--	50@	20	
TOTAL				18	01	15		500		50		50		200		50
STUDENT CONTACT HOURS PER WEEK: 34 HRS																
THEORY AND PRACTICAL PERIODS OF 60 MINUTES EACH.																
TOTAL MARKS: 850 @ INTERNAL ASSESSMENT # EXTERNAL ASSESSMENT																
ABBREVIATIONS: TH-THEORY, TU- TUTORIAL, PR-PRACTICAL, ,OR-ORAL, TW-TERMWORK, SW-SESSIONAL WORK																
<ul style="list-style-type: none"> ➤ Conduct two class tests each of 25 marks for each theory subject. Sum of the total test marks of all subject are to be converted out of 50 marks as sessional work. ➤ Progressive Evaluation Is To Be Done By Subject Teacher As Per The Prevailing Curriculum Implementation And Assessment Norms 																